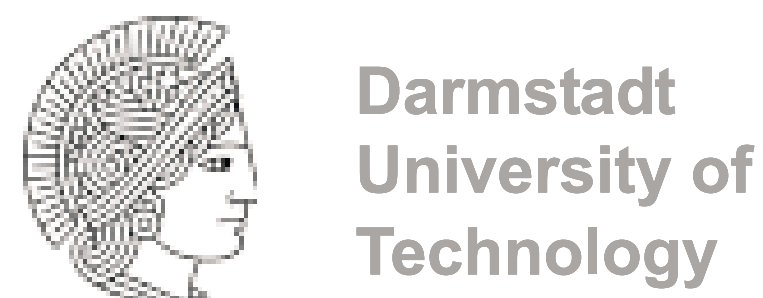
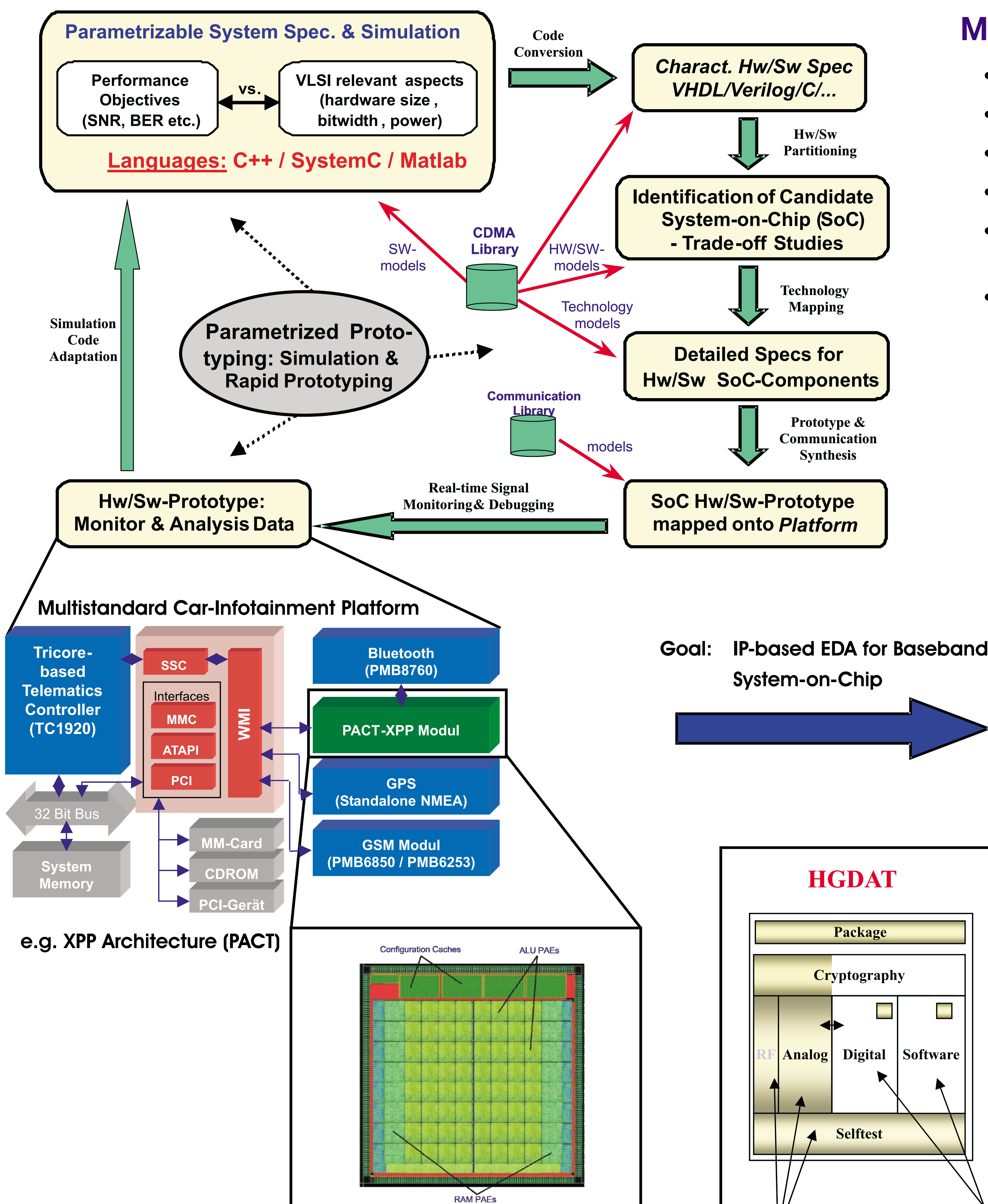


BMBF / EkompasS: IP²: Intellectual Property EDA for Configurable Baseband SoCs in Internet Protocol Mobile Networks



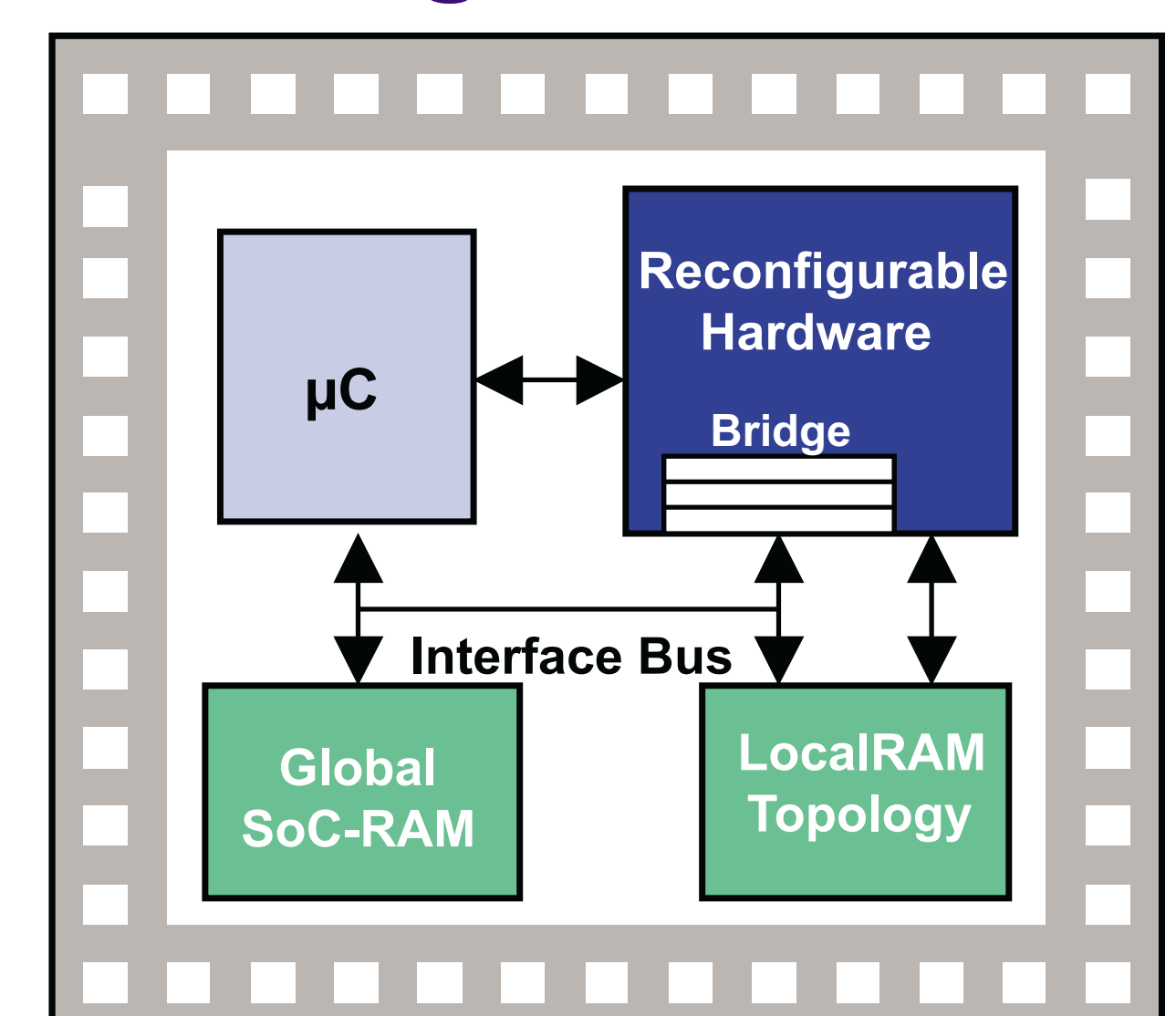
Parametrized IP-based EDA Flow



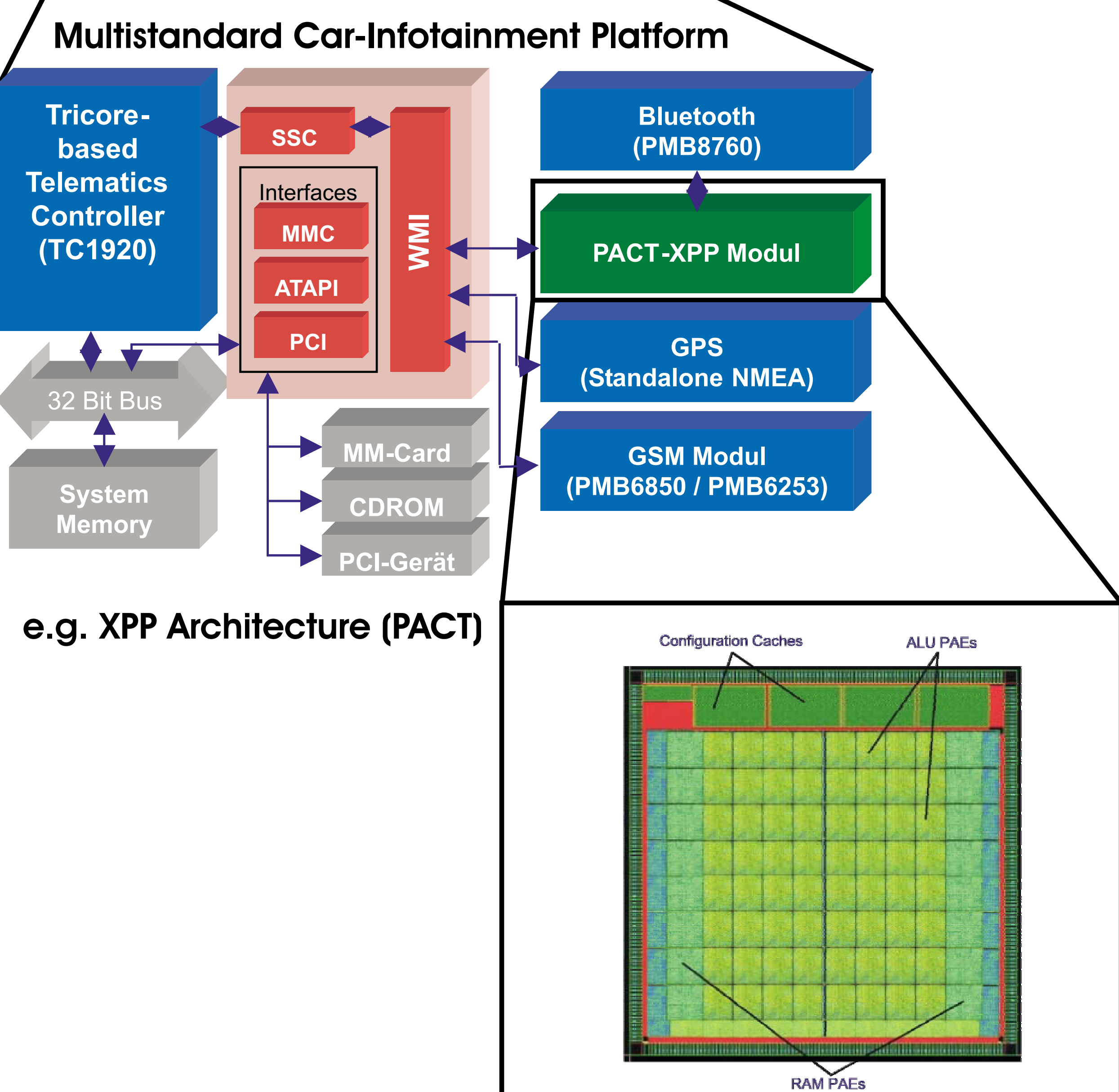
Application-specific EDA for Mobile Communication Systems

- shorter system development cycles
- shorter microelectronic integration of IP-cores
- shorter turnaround time for redesigns
- efficient development of generic IP-cores
- usage of generic reusable IP-cores in industrial EDA environments
- driving european standardization

Configurable SoC



Goal: IP-based EDA for Baseband System-on-Chip



Fine/Coarse Grain Reconfigurable Hardware

- flexible digital baseband processing
- flexible switching of hardwired and reconfigurable hardware modules

