

8:30 am **Come Together**
Company Presentations Salon V/VI
 Chairman: Jürgen Haase

10:00 am **Cadence Design Systems, Inc.:**
 "Increasing the Productivity of AMS-Designs by Leveraging Constraint Driven Design"

10:35 am **Coffee Break**

10:55 am **Synopsys, Inc.:**
 "Predictable Success: Today You Will Show Impossible the Door"

11:30 am **Magma Design Automation, Inc.:**
 "True Design Automation - "Complete any Chip in 2 Days or less"

12:05 pm **Lunch**
Welcome Address Saal B
 Wolfgang Rosenstiel, Chairman edacentrum

1:30 pm **General Keynote** Saal B
Aldo Romano, President & CEO STMicroelectronics Italy:
 "The Innovation Process in the Semiconductor Industry: the Example of STMicroelectronics"

Technical Session I
 "NOTHING ELSE MATTERS"
 Reliable Systems Don't Come for Free
 Chairman: Peter van Staa

2:40 pm **Keynote** Salon V/VI
 Stefan Lottholz, Bosch:
 "Automotive Electronics - High Performance Reliability at Attractive Price"

3:30 pm **Coffee Break**

4:00 pm **Georg Georgakos, Infineon:**
 "VLSI Reliability Aspects from a Transistor's Perspective"

4:35 pm **Michael Müller, IBM:**
 "RAS Design Overview of the 'IBM System z'"

5:10 pm **Break**

5:20 pm **Panel Discussion** Saal B
 Wolfgang Rosenstiel, edacentrum:
 "What are the top EDA challenges in 2007?"

6:20 pm **Break**

7:00 pm **Social Event** Meeting Point - Hotel Reception
Aquarium Candle-light Dinner and a performance by Jaqueline Boulanger

Technical Session II
 "THE SHOW MUST GO ON"
 Exploring the New World - Technologies beyond CMOS
 Chairman: Erich Barke

9:00 am **Keynote** Salon V/VI
 Paolo Lugli, Technical University of Munich:
 "Recent Advances in Nanoelectronics"

9:50 am **Johannes Windeln, IBM:**
 "Key Enabler for Future Technologies"

10:25 am **Coffee Break**

10:55 am **Henning Riechert, Qimonda:**
 "Scaling CMOS down to the Nanometer Range"

11:30 am **Wolfgang Porod, University of Notre Dame:**
 "Circuit Architectures for Nanoscale Electronics"

Business Session II
 "WITH A LITTLE HELP FROM MY FRIENDS"
 Leadership Means Partnership
 Chairman: Albert Stritter

9:00 am **Keynote** Saal B
 Barry Dennington, NXP Semiconductors:
 "The Partnership Network"

9:50 am **Thomas Harms, Infineon:**
 "Improving the Partnerships in the Design System Value Chain - an Infineon Perspective"

10:25 am **Wayne Howell, IBM:**
 "The Cell Processor: Partnership for Success"

11:30 am **Sean Redmond, Cadence:**
 "EDA Leadership - A Journey through the Electronic Design Chain"

Friday Keynote Saal B
Moshe Gavrielov, Executive Vice President, Cadence:
 "System Verification: THE Enterprise Level Development Problem"

Closing Words Saal B
 Erich Barke, Vice Chairman edacentrum

Lunch 1:15 pm

Trips & Tours 2:30 pm - 5:30 pm and 7:00 pm respectively
 Visit of the Reichstag building (3.00 - 7:00 pm)
 Exhibition "The Story of Berlin" (2.30 - 5:30 pm)

Registration Opening Time Floor

1st day: 8:30 am - 5:30 pm	2nd day: 8:30 am - 9:30 am & 12:30 pm - 2:00 pm
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EDA Lounge Salon IV

Aiming to create an area for communication in a relaxed atmosphere, the EDA Lounge will be a meeting place inviting to sit down and talk to colleagues, partners and company representatives. The EDA Lounge will accompany the whole event.

1st day: 8:30 am - 6:00 pm	2nd day: 8:30 am - 1:00 pm
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