

4th European RISC-V Firmware and Embedded Rust Workshop (RVF 2026)

Content note: AI-assisted. Curated & edited by Henri Lunnikivi, reviewed by Raphael Kunz.

Important dates

Abstract submission	31 August 2026
Author notification	21 September 2026
Full paper (CEUR-WS)	to be announced
Workshop	19 October 2026

Venue:

Co-located with IEEE NorCAS 2026
(20–21 October 2026),
Tampere University, Finland

About the Workshop

First introduction of the RVF brought together researchers and engineers working on firmware for RISC-V and on embedded programming in Rust. These topics are gaining traction due to the increasing popularity of RISC-V and the more stringent requirements onto embedded programming, which is reflected in the work packages of several European and national projects, addressing firmware generation, hardware abstraction layers, runtime environments, virtual prototyping, and validation for RISC-V-based systems.

RVF provides a forum for early results, work in progress, and open discussion. A place to present what you're building before it's finalized, compare notes, and find collaborators working on adjacent problems. The 4th edition of the RVF this year runs as a full-day event co-located with the IEEE Nordic Circuits and Systems Conference (NorCAS) in Tampere.

For mature contributions, the RVF team is organizing a special session at the co-located NorCAS conference, which follows a fully peer-reviewed conference paper track and will be included in the IEEE-conference proceedings.

The two tracks are complementary:

- **RVF** for early ideas and discussion with an option to publish at CEUR-WS (5+ pages, single column),
- **the NorCAS special session** for a polished 4–6-page IEEE-format paper and a conference presentation slot.

Topics of interest

Contributions are welcome on any aspect of RISC-V firmware and embedded Rust, including but not limited to:

- Firmware development for RISC-V in any language: generation, libraries, and validation
- Embedded Rust hardware abstraction layers, drivers, and runtime environments
- Rust co-execution and tool interoperability with C/C++
- Toolchain, build-system, and EDA support for Rust
- Instruction-set simulators, host emulation, and virtual prototyping with Rust support
- RISC-V ISA extensions, accelerators, and their firmware/software implications
- Security, safety qualification, and certification of RISC-V/Rust firmware, including in light of the EU Cyber Resilience Act
- Rust in embedded systems education and adoption experience
- Open-source firmware ecosystems for RISC-V

Format

RVF runs as a full day (09:00–17:00) of talks and discussion, with coffee and lunch provided at the venue. Presentations are typically short talks followed by open discussion; the exact schedule will depend on the number and nature of accepted contributions.

Call for Contributions

We invite submission of an **abstract** describing your contribution. Abstracts are the only thing required to be considered for a talk slot.

If your abstract is accepted, we additionally invite you to submit a **full paper** (PDF and LaTeX source, at least 5 pages, single column) for inclusion in a peer-reviewed **CEUR-WS proceedings volume**. This step is optional: you're welcome to present at RVF from an abstract alone, but if you'd like a citable, archival record of your work, this is the path for it.

- Full papers must use the CEURART style, available via [OverLeaf](#). More information is available at <https://ceur-ws.org/HOWTOSUBMIT.html#CEURART>
- Publication is under the CC-BY license via CEUR-WS's standard author agreement: ceur-ws.org/ceur-editor-agreement-ccbby.pdf.
- Full papers will undergo peer review before inclusion in the proceedings.

Submission

Submit your abstract via the NorCAS conference submission system at conftool.pro/norcas2026, selecting as the target track: "4th European RISC-V Firmware and Embedded RUST Workshop".

Venue and attendance

The workshop takes place at the **Tampere University city campus main building**, across the street from the NorCAS venue (Tampere Hall).

To register for the workshop, you can either add the workshop to your NorCAS registration, or register for the workshop only. The fee is **100 EUR** (IEEE members) or **150 EUR** (regular). Register at lyyti.fi/reg/norcas2026/en.

If you also plan to attend the full NorCAS conference (20–21 October), the same registration page lets you combine both.

Organizing Committee

- Henri Lunnikivi, Tampere University (FI), henri.lunnikivi@tuni.fi
- Raphael Kunz, Infineon Technologies AG / TU Munich (DE), raphael.kunz@infineon.com
- Per Lindgren, Luleå University of Technology (SE), per.lindgren@ltu.se
- Pawel Dzialo, Luleå University of Technology (SE), pawel.dzialo@ltu.se
- Malte Munch, Luleå University of Technology (SE), malte.munch@ltu.se
- Andreas Vörg, edacentrum (DE), voerg@edacentrum.de

We're grateful to General Chair of NorCAS, **Jari Nurmi**, for enabling this co-location and the workshop venue, lunch, and coffee.