ICT Research Funding: New Calls for Proposals

The ICT Work Program 2009/2010 describes the challenges and objectives for ICT research funding for the next two years

Call Title	Call Identifier	Date of Publication	Deadline	Indicative Budget
ICT Call 4	FP7-ICT-2009-4	November 19, 2008	April 1, 2009	EUR 801 million
ICT Call 5	FP7-ICT-2009-5	July 31, 2009	November 3, 2009	EUR 722 million
ICT Call 6	FP7-ICT-2009-6	November 24, 2009	April 13, 2010	EUR 286 million
FET Open	FP7-ICT-2009-C	November 19, 2008	December 31, 2010	EUR 61 million
Joint Call ICT & Energy	FP7-ICT-ENERGY-2009-1	December 8, 2008	March 31, 2009	EUR 20 million

Table E.01: Summary of calls for the ICT Work Program 2009-10 [1]

Are you interested in responding to calls for proposals for projects in the field of Information and Communication Technologies (ICT)? Then edacentrum suggests having a closer look at the recently published ICT Work Program 2009-10.

The ICT Work Program 2009-10 for the ICT theme of the FP7 Specific Program 'Cooperation' defines the priorities for calls for proposals closing in 2009 and 2010. The work program is divided into 7 Challenges, Future and Emerging Technologies (FET) and Horizontal Support Actions. The structure is as follows:

» Two calls with high budget (ICT Call 4 and 5)
» One call with a smaller budget (ICT Call 6)
» A continuously receivable call (FET Open)
» A joint call for ICT and Energy (Joint Call ICT & Energy)

Objectives for nanoelectronics

The edacentrum already had a closer look at the work program and has identified the following interesting objectives from a nanoelectronics perspective.

» Objective 3.2 (Call 4): Design of Semiconductor Components and Electronic Based Miniaturized Systems
This objective addresses generic platforms, methods and tools to cope with the design challenges in the next generations of technologies and with heterogeneous integration of different functions. The activities in this area are complementary to the activities in the ENIAC JTI. The indicative budget is EUR 25.0 million and splits into EUR 21.5 million for IP and STREP (at least one IP required) and EUR 3.5 million for CSA.
» Objective 3.4 (Call 4): Embedded Systems Design
The activities in this area aim at novel generic methods and integrated design environments that are applicable across application sectors; they are complementary to the activities in the ARTEMIS JTI, where embedded system design is

not addressed as such but only in the context of specific applications. The indicative budget is EUR 28.0 million and splits into EUR 27.25 million for IP and STREP (at least two IP required) and EUR 0.75 million for CSA.
» Objective 6.3 (Call 4): ICT for Energy Efficiency
The activities in this area aim at ICT support to energy-positive buildings and neighborhoods and ICT services and software tools enhanced with energy features. The indicative budget is EUR 30.0 million and splits into EUR 27.0 million for STREP and EUR 3.0 million for CSA (CA only).

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