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# Scaling CMOS down to the Nanometer Range

Henning Riechert

## Abstract

This talk will give a brief overview of nanoscale CMOS transistors and the main problems encountered in downscaling CMOS. While the geometrical rules for downscaling are well-known, the actual reduction of sizes hits the famous



edacentrum | Schneiderberg 32 | 30167 Hannover | fon: +49 511 762-19699 | fax: +49 511 762-19695 | email: [info@edacentrum.de](mailto:info@edacentrum.de)  
edacentrum [dot] [denach oben](http://www.edacentrum.de)

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