

Modeling and the Road towards True Synthesis of Analog Integrated Circuits

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Abstract

This presentation will give a short overview of modeling techniques for analog integrated circuits, ranging from symbolic analysis approaches to more recent advanced black-box approaches. The role of modeling in the design flow will be described, and the perspectives it offers towards circuit optimization and true structural synthesis will be lined out

Curriculum Vitae



Georges G.E. Gielen is a Professor in Electrical Engineering at the Katholieke Universiteit Leuven, Belgium. His research interests are in analog and mixed-signal design and design automation. He is responsible for many industrial projects in this area, he has published more than 200 papers in books, journals and conference proceedings, and is invited regularly for program committees of conferences and for presenting tutorials and invited speeches on analog and mixed-signal design and CAD. He is a member of the Board of Governors of the IEEE Circuits and Systems Society. He was the 1997 Laureate of the Belgian National Academy of Science, Literature and Arts in the category of engineering sciences, and he received the 2000 Alcatel Award for his innovative research in telecommunications from the Belgian National Fund of Scientific Research. He is a Fellow of the IEEE.