

About edaWorkshop

The edaWorkshop is the premier German EDA event for the publication and discussion of application-oriented EDA research findings. The mix of representatives from industry and research creates ideal opportunities for a professional exchange of ideas on a scientific basis. The dialog can pave the way for industry to benefit from research results.

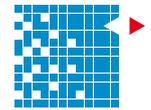
The edaWorkshop is also the primary platform for presenting and exchanging solution approaches and results of EDA projects funded by the BMBF (German Ministry of Education and Research). It promotes communication between EDA experts and

public authorities, and supports the dissemination of the results of publicly-funded projects.

This two-day event is a balanced combination of information and communication. It not only offers a wide range of discussions on specialized subjects and EDA research projects, but also provides plenty of networking opportunities. This is supported by a comprehensive poster exhibition, where demonstrations and prototypes will also be presented and by the successful cooperation marketplace entitled "Ideas in search of users – market in search of innovations".

At the edaWorkshop, you can meet not only your regular colleagues and acquaintances, but also those people you have otherwise known only by phone and email. In addition, you will have the opportunity to make new contacts, in particular with industry.

The edaWorkshop is organized jointly by the edacentrum and BMBF, DLR and the GI/GMM/ITG RSS Steering Group for "Computer-aided Circuit and System Design". In 2009, the edaWorkshop will co-locate and share a common day – including keynotes, sessions and the social event – with the annual CATRENE/MEDEA+ Design Automation Conference.



edaWorkshop 09

Scientists and users are invited to submit contributions; EDA project teams are invited to present their results in talks and on posters. You will have plenty of opportunity to bring your topics and results to the edaWorkshop. You can find the details under „Submission of contributions“.

We are looking forward to your contribution!



ITG



GMM



Submission of contributions

In addition to the presentation of EDA research projects and their results, the edaWorkshop aims at the publication of industrially-relevant R&D results covering topics listed overleaf.

Scientists and users are invited to submit contributions without author and company names in five to six pages, in German or English, at www.edacentrum.de/edaworkshop/upload/. Guidelines for authors as well as the templates (Word and LaTeX) can be found at: www.edacentrum.de/edaworkshop/call/.

For the latest information see: www.edacentrum.de/edaworkshop

Expected are:

1. Scientific contributions from research and industry which present new EDA research and development results
2. Presentations of EDA topics of a visionary or survey character, with scientific or practical impact
3. Contributions concerning the application relevance and/or the economic impact of technical challenges and solutions
4. Reports on experiences or on the dissemination of results from industrial practice
5. Demonstrations of research and development results, in particular those from Ekompass projects

The program committee, consisting of leading EDA experts (named overleaf) from industry and research, will review the contributions by category in order to devise a program of presentations, posters and demonstrations.

Accepted contributions will be published in the edaWorkshop proceedings, which will appear in the VDE-Verlag with an ISBN. The proceedings will not distinguish between poster, presentation and demonstration contributions – all contributions are equally important to our common goal.

Key dates

November 11, 2008

Submission of papers

December 19, 2008

Notification of acceptance

February 16, 2009

Submission of camera-ready papers

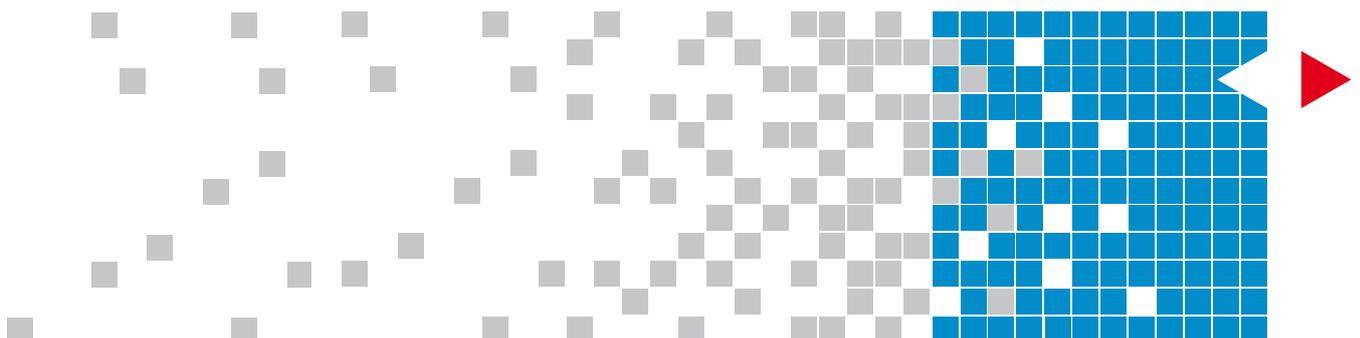
May 26 – 28, 2009

edaWorkshop in Dresden

Contact

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Call for Papers



edaWorkshop 09

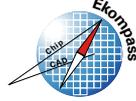
Dresden, May 26 – 28, 2009

The edaWorkshop - catalyst of EDA research

The design of integrated circuits and systems places enormous demands on R&D engineers and the design methods and tools that they use. It requires the efficient and manufacturing-aware development of safe, economical, robust and reliable systems of high complexity with very small structures (< 90 nm), and the design of analog and mixed-signal circuits.

In order to stimulate EDA research activities to deal with these challenges, the BMBF (Federal Ministry for Education and Research) has established the R&D program Ekompas (Entwurfplattformen komplexer angewandter Systeme und Schaltungen – design platforms for complex applied

systems and circuits). In Ekompas industry and research join forces with the public authorities to support those areas which are vital for the competitiveness of German industry.



The Ekompas support activity is carried out within the new BMBF research program for information and communication technology (IKT2020). This program, which was compiled in collaboration with science and industry, and presented in 2007, focuses on five application fields with a high potential added value, and with considerable potential for job

creation. These fields are: automobile/mobility, mechanical engineering/automation, health/medical technology, logistics/services and energy/environment.

The edaWorkshop is the central platform for exchanging information about the approaches and results of Ekompas projects. For this purpose, people involved in the projects are invited to present their results by means of talks and posters. The relevance of the application to topics affecting society should be at the heart of these presentations (see IKT 2020). At the same time, the edaWorkshop is a scientific workshop where also other R&D results can and should be presented.

CATRENE joint event

The edaWorkshop09 is jointly organized with the annually CATRENE/MEDEA+ Design Automation Conference (CATRENE/MEDEA+ DAC). The joint event will give a comprehensive overview of latest algorithms and tools, emerging technologies, key CATRENE/MEDEA+ and Ekompas projects, and advanced research in application-oriented SoC design automation in Europe. The joint event will be divided into a CATRENE/MEDEA+ DAC day (May 26), an edaWorkshop day (May 28), and a joint day common to both (May 27).

For information on CATRENE see www.catrene.org.

edaWorkshop09 Topics

Submissions covering the following topics are welcome:

System Level and Hardware/Software Design of Embedded Systems

- Application-oriented EDA
- Specification- and Model-based Design
- Architectural Synthesis and Optimization
- Advanced Architectures (ASIPs, SoCs, MPSoCs, NoC, SiPs and Reconfigurable Architectures)
- Transaction Level Modeling and Simulation
- Development and Optimization of Hardware-dependent Software
- Analysis and Optimization of Performance and Power

- Design Automation for Analog Circuits
- Simulation and Verification
- RF Circuits, Smart Power Circuits
- Model Generation
- Parasitics and Interconnects, Signal Integrity

Analog- and Mixed-Signal Design

- Simulation Acceleration
- Formal Verification
- Timing Analysis
- Low Power Design, Analysis and Optimization
- LogicSynthesis and Optimization
- Rapid Prototyping

Design and Verification

Test and Reliability

- Innovative Test Methods
- System and Industrial Test
- Design for Reliability, Design for Testability and BIST
- Test Generation, Diagnosis and Fault Modeling
- Statistical, Physical and Defect-oriented Testing
- Test of Regular Structures

- Logic- and Technology-dependent Synthesis for Deep-Submicron Circuits
- Physical Design and Verification
- EMC and Packaging
- Design Centering and Yield Optimization (DfM)
- Statistical Timing Analysis and Variability

Manufacturing-aware Design

Committees of the edaWorkshop

Program Committee

Chair:

- N. Wehn, TU Kaiserslautern

General Chairs of the edaWorkshop

- E. Barke, U Hannover, edacentrum
- W. Rosenstiel, U Tübingen, edacentrum

- W. Anheier, U Bremen
- H.-J. Brand, AMD Saxony LLC & Co. KG
- O. Bringmann, Research Center f. Information Technology (FZI)
- W. Daehn, University of Applied Sciences Magdeburg-Stendal
- M. Dietrich, Fraunhofer Institute for Integrated Circuits
- R. Drechsler, U Bremen
- W. Ecker, Infineon Technologies AG
- K. Eckl, Synopsys GmbH
- R. Ernst, TU Braunschweig
- H. Eweking, TU Darmstadt
- D. Friebel, VDE/VDI-GMM
- W. Glauert, U Erlangen-Nürnberg
- H. Gräß, TU München
- K. Hahn, U Siegen
- L. Hedrich, U Frankfurt
- E. Hennig, Melexis GmbH
- A. Herkersdorf, TU München
- S. Huss, TU Darmstadt
- E. Jentzsch, Cadence Design Systems GmbH
- S. Kern, Atmel Germany GmbH
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- W. Nebel, OFFIS - Institute for Computer Science
- R. Pferdmenges, Infineon Technologies AG
- I. Rugen-Herzig, Infineon Technologies AG
- S. Sattler, Infineon Technologies AG
- J. Schlöffel, Mentor Graphics GmbH
- P. van Staa, Robert Bosch GmbH

- J. Haase, edacentrum
- R. Popp, edacentrum
- D. Treytnar, edacentrum
- L. Wenzel, PT-DLR
- P. Federer, GI
- V. Schanz, ITG in VDE
- R. Schnabel, VDE/VDI-GMM

Organization Committee