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IPQ: IP qualification for efficient system design



The IP-based system-level modeling and simulation design methodology represents a breakthrough in current design methodology, which has a strong impact on competitiveness. The main goal of this project is to address the qualification and reuse of intellectual property (IP) in current system design in networking, high-speed links, multimedia, and automotive domains by using system-level modeling and verification techniques. This will include design reuse with qualified and parametric IP cores and a seamless design flow integrating existing and emerging tools. It will enable the improvement of developed tools and possible future commercialization.

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Project Information

Project partners:

- [AMD Saxony LLC & Co. KG](#)
- [Deutsche Thomson OHG](#)
- [empolis knowledge management GmbH](#)
- [FZI Forschungszentrum Informatik](#)
- [Infineon Technologies AG](#)
- [sci-worx GmbH](#)
- [Siemens Business Services GmbH & Co OHG](#)
- [Universität Paderborn](#)

[Final Report](#)

[Project Flyer \(DE\)](#)

[NL 02 2004 \(PN\)](#)

[NL 01 2004 \(PN\)](#)

[NL 03 2003 \(PN\)](#)

[NL 02 2003 \(PB\)](#)

[NL 02 2002 \(PN\)](#)

[NL 01 2002 \(PN\)](#)

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Website:

Used Abbreviations

Abbreviation	Meaning
PR	Project Report
SPR	Short Project Report
PN	Project News
FPR	Final Project Report

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