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edaWorkshop11 and CATRENE DTC - Program

At this web page you find the programme of the edaWorkshop11 and CATRENE DTC. You may expand the programme for each session by clicking on the session title. You will find the detailed timetable, presentation titles and author names. If additional information like an abstract, curriculum vitae or (for attendees of the edaWorkshop11 and CATRENE DTC only) slides is available, a link below the presentation title is displayed.

You may [download](#) the programme as PDF file (1.90 MB).

Tuesday, May 10, 2011

09:15 - 10:30

Keynote

Moderator: Erich Barke (edacentrum)

09:15 **Welcome**

More than Moore - Challenges and Opportunities for Europe

09:30 Michel Brillouët (LETI)

[Abstract and Curriculum Vitae](#)

Real-world Applications of Smart Environments in the Home

Bastiaan de Groot (Philips)

Examples and future visions of Smart Environments in the home have been around for a long time, but despite our fast advancement in reducing chip cost, power usage and processing power, only a fraction of the envisioned use cases have been realized. This is mainly caused by the high level of integration on a semantic level that is needed to realize many of the use cases and the absence of applications that could justify the investments in a backbone infrastructure for the home. This is currently changing rapidly, with the increasing societal cost of energy and an aging population, there suddenly is the societal need that justifies the installation of the needed infrastructure in the home. With emergence of standards like Zigbee and Continua we are also well on our way to solve the problem of semantic standardization for these domains. The interesting question that remains is how the end-user and other stakeholders will react to these developments, with the pressure to change mainly being driven by a societal need and not by an end-user insight, these developments are met with great skepticism by the end-users (as we can see with the introduction of for example the smart meter). The major question will be how to involve the end-user in such a way that he will embrace this type of solutions and this is (usually) not the case when the only benefit is financial. Only if the solutions add true value to the lives of the consumers they will fully embrace the solutions developed, something which is often overlooked in many societal driven developments. In this talk an outline will be given of end-user driven developments in this field from the perspective of Philips Research.

[Download Presentation](#)

10:30 - 11:00

Coffee Break

11:00 - 12:30

Technical Session: Healthcare

Moderator: Paul Merkus (Philips)

THOR: Striking Technologies for Power
11:00 Mark van Helvoort (Philips)

CSI: Central Nervous System's Multi Modal Imaging
11:35 Roberto Zafalon (ST Microelectronics)

MAS: Mobile Ambient Assisted Living
12:00 Ovidiu Vermesan (SINTEF, NO)
[Download Presentation](#)

12:25 **Q&A**

12:30 - 14:00
Lunch

14:00 - 15:30

Technical Session: System Design Methodologies

Moderator: Frédéric Pétrot (TIMA/INPG)

14:00 **Multimedia Box to Home System**
Dominique Hénoff (ST Microelectronics)

14:35 **Hardware Dependent Software Solutions for SoC Design**
Ervin de Kock (NXP)

An Approach toward Accurately Timed TLM+ for Embedded System Models
Kun Lu (TU Muenchen)
15:00 Daniel Müller-Gritschneider (Technical University of Munich, D)
Wolfgang Ecker (Infineon, D)
Volkan Esen (Infineon, D)
Michael Velten (Infineon)
Ulf Schlichtmann (TU München, D)

15:25 **Q&A**

15:30 - 16:00
Coffee Break

16:00 - 17:30

Technical Session: Multicore Architecture

Moderator: Denis Rousset (ST Ericson)

The CC-Numa TSAR Machine
Kun Lu (TU Muenchen)
Daniel Müller-Gritschneider (Technical University of Munich, D)
16:00 Wolfgang Ecker (Infineon, D)
Volkan Esen (Infineon, D)
Michael Velten (Infineon)
Ulf Schlichtmann (TU München, D)

P2012 in COBRA Project: Processing Array for Computing-intensive Applications
16:35 Fabien Clermidy (CEA Tech, F)

COMCAS Project: Energy Efficient Devices for Digital Entertainment
17:00 Armand Castillejo (ST Ericsson)
[Download Presentation](#)

17:25 **Q&A**

17:30 - 18:30

Panel: Closing the gap between technology and application needs

Moderator: Bernard Candaele (Thales, F)

Network on Chip - Alternative Solutions and Future Directions

17:30 Cédric Plomion (ARTERIS)
Pierre Bricaud (Synopsys)
Marcello Coppola (STMicroelectronics, F)
Fabien Clermidy (CEA Tech, F)
Ian O'Connor (Ecole Centrale de Lyon)

18:30 - 19:30

Break

19:30 - 23:00

Conference Dinner

Altmarkt 4, 01067 Dresden, Germany (Altmarktkeller)

19:30 **Meeting point at hotel reception**

19:45 **Arrival at „Altmarktkeller“**, Altmarkt 4, 01067
Dresden

20:00 **Dinner:**
Dinner

23:00 **End of 1st day**

Wednesday, May 11, 2011

09:00 - 10:00

Keynote

Moderator: Wolfgang Rosenstiel (edacentrum, D)

Gaining 10x in Power Efficiency in the Next Decade in Consumer Products

09:00 Gerd Teepe (GLOBALFOUNDRIES, D)
Abstract and Curriculum Vitae

10:00 - 10:15

Coffee Break

10:15 - 11:45

Technical Session: High Level AMS Design

Moderator: Serge Scotti (ST Microelectronics)

Design Refinement of Embedded Analogue and Mixed-Signal Systems

Serge Scotti (ST Microelectronics)
10:15 Martin Barnasconi (NXP)
Marie-Minerve Louërat (U Pierre & Marie Curie, Paris)
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A Performance Comparison Between the SystemC-AMS Models of Computation

10:50 Franck Paugnat (TIMA/INPG)
L. Bousquet (TIMA/INPG)
Katell Morin-Allory (TIMA/INPG)
Laurent Fesquet (TIMA/INPG)

Virtual Prototype of a Fibre-optical Gyrosensor with SystemC-AMS

11:15 Stefan Rieke (LITEF)
Oliver Waydhas (LITEF)
Demo at Poster Exhibition

11:40 **Q&A**
Serge Scotti (ST Microelectronics)

11:45 - 12:00

Poster Session

Moderator: Ralf Popp (edacentrum, D)

Comprising accepted scientific contributions (for details see bottom of next page) and an overview on project posters and demonstrations

Introduction the Poster

11:45 **Exhibition**
Ralf Popp (edacentrum, D)

12:00 - 12:30

Poster Exhibition

Within the Poster Exhibition we will have posters concerning the following scientific contributions: Besides these reviewed contributions to edaWorkshop11, the poster exhibition will show posters and demonstrations of all EDA projects funded by BMBF within IKT 2020

Poster Session:

12:00 **A Methodology to Use Classical IP Blocks in 3D Circuits**
Johann Knechtel (TU Dresden)
Jens Lienig (TU Dresden, D)

Poster Session:

Robustness Evaluation of Embedded Software Systems
Weiyun Lu (U Stuttgart)
12:00 Malte Metzdorf (OFFIS)
Domenik Helms (OFFIS, D)
Martin Radetzki (U Stuttgart)
Wolfgang Nebel (OFFIS)

Poster Session:

Shared-memory Communication in Distributed SoCs on Multi-FPGA Systems
12:00 Marcus Müller (TU Ilmenau)
Oliver Brandel (TU Ilmenau)
Wolfgang Fengler (TU Ilmenau)

Poster Session:

Fully Coupled Circuit and Device Simulation with Exploitation of Algebraic Multigrid Linear Solvers
12:00 Bernhard Klaassen (Fraunhofer SCAI)
Tanja Clees (Fraunhofer SCAI)
Monica Selva Soto (Fraunhofer SCAI)
Caren Tischendorf (U Köln)

Poster Session:

Impact Estimation for Design Flow Changes
12:00 Roland Koppe (OFFIS)
Stefan Häusler (OFFIS)
Frank Poppen (OFFIS)
Axel Hahn (OFFIS)

12:30 - 14:00

Lunch and Poster Exhibition

14:00 - 15:30

Technical Session: Design for Yield and Reliability

Moderator: Georg Georgakos (Infineon)

14:00 **Design Methods to Influence SoC Quality, a Post HONEY View**
Georg Georgakos (Infineon)

14:35 **Efficient and Reliable Feeding of the EDA Flow with Process Data**
Roberto Gonella (ST Microelectronics)

Methodology for Experimental Investigation of Matching and Ageing Effects
15:00 Boyko Dimov (IMMS)
Eckhard Hennig (IMMS)

15:25 **Q&A**
Georg Georgakos (Infineon)

15:30 - 16:00

Coffee Break

16:00 - 17:30

Panel: More than Moore – the Most Important Driver of Microelectronics! Or?

Moderator: Dirk Friebe (Fraunhofer-IIS/EAS)

**More than Moore – the Most Important Driver of Microelectronics!
Or?**

Hans-Jürgen Brand (GlobalFoundries)

Volker Herbig (X-FAB)

16:00 Thomas Hötzel (Atmel)

Serge Scotti (ST Microelectronics)

Steffen Kroehnert (Nanium, PRT)

Peter Schneider (Fraunhofer-IIS/EAS)

Wolfgang Stronski (Cadence)

17:30 - 18:00

Break

18:00 - 23:00

Social Event

Grunaer Str. 14, Dresden, Germany (Meeting point at Dorint Hotel Dresden reception)

18:00 **Meeting point at hotel reception for guided tour**

18:15 **Guided tour at „Deutsche Hygiene-Museum“, Lingnerplatz 1, 01069
Dresden**

19:00 **Meeting point at hotel reception**

19:15 **Arrival at „Deutsche Hygiene-Museum“, Lingnerplatz 1, 01069 Dresden**

Social Event:

19:30 **Award of "EDA-Medaille 2011"**

[Further Information](#)

19:45 **Dinner:**

Dinner

23:00 **End of 2nd day**

Thursday, May 12, 2011

09:00 - 09:45

Keynote

Moderator: Ulf Schlichtmann (TU München, D)

**Design and Verification of Dependable Analog
Systems**

09:00

Lars Hedrich (U Frankfurt, D)

[Abstract and Curriculum Vitae](#)

09:45 - 10:15

Coffee Break

10:15 - 12:00

Technical Session: Robust Design

Moderator: Christian Sebeke (Bosch)

**Design Methods for ROBUST Electronic Systems - Searching for the Holy
Grail?**

10:15

Volker Schöber (edacentrum)

An NBTI Model for Efficient Transient Simulation of Analogue Circuits

10:50

Felix Salfelder (U Frankfurt)

Lars Hedrich (U Frankfurt, D)

Error Prediction Based on Concurrent Self-test and Reduced Slack Time

Valentin Gherman (CEA-LIST)

- 11:15 J. Massas (CEA-LIST)
S. Evain (CEA-LIST)
Stéphane Chevobbe (CEA-LIST, F)
Y. Bonhomme (CEA-LIST)

Social Event:

- 11:40 **Award of "EDA Achievement Award 2011"**
[Further Information](#)

12:00 - 14:15

Poster Exhibition

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Poster Session:

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Johann Knechtel (TU Dresden)
Jens Lienig (TU Dresden, D)

Poster Session:

- Robustness Evaluation of Embedded Software Systems**
Weiyun Lu (U Stuttgart)
12:00 Malte Metzdorf (OFFIS)
Domenik Helms (OFFIS, D)
Martin Radetzki (U Stuttgart)
Wolfgang Nebel (OFFIS)

Poster Session:

- Shared-memory Communication in Distributed SoCs on Multi-FPGA Systems**
12:00 Marcus Müller (TU Ilmenau)
Oliver Brandel (TU Ilmenau)
Wolfgang Fengler (TU Ilmenau)

Poster Session:

- Fully Coupled Circuit and Device Simulation with Exploitation of Algebraic Multigrid Linear Solvers**
12:00 Bernhard Klaassen (Fraunhofer SCAI)
Tanja Clees (Fraunhofer SCAI)
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Caren Tischendorf (U Köln)

Poster Session:

- Impact Estimation for Design Flow Changes**
12:00 Roland Koppe (OFFIS)
Stefan Häusler (OFFIS)
Frank Poppen (OFFIS)
Axel Hahn (OFFIS)

13:00 - 14:15

Lunch and Poster Exhibition

14:15 - 15:50

Technical Session: Analog Synthesis and Optimization and 3D-Circuits

Moderator: Lars Hedrich (U Frankfurt, D)

SyEnA Drives Synthesis-Supported Design of Analog Circuits

- 14:15 Achim Graupner (ZMD)
[Download Presentation](#)

Reliability Optimization of Analog Circuits with Aged Sizing Rules and Area Trade-off

- 14:50 Xin Pan (TU Muenchen)
Helmut Gräß (TU München)

3D Physical Design: Challenges and Solutions

15:15 Robert Fischbach (TU Dresden)
Jens Lienig (TU Dresden, D)
Tilo Meister (TU Dresden)

15:40 **Closing**
Jürgen Haase (edacentrum, D)

15:50 **End of 3rd day**

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