

Veröffentlichungen

2017

- (1) Strobel, M., Radetzki, M.; Hybrid Instruction Set Simulation for Fast and Accurate Memory Access Profiling; Proceedings of the 13th Workshop on Intelligent Solutions in Embedded Systems (WISES); Jun 2017
- (2) Herdt, V., Le., M. H., Große, D., Drechsler, R.; Towards Early Validation of Firmware-Based Power Management using Virtual Prototypes: A Constrained Random Approach; Proceedings of the Conference Forum on specification & Design Languages (FDL); Sep 2017
- (3) Martin Dittrich; Driver Generation and Optimization of the SW/HW Interface Tutorial Talk on Design and Verification Conference (DVCON) Europe 2017;
- (4) Drechsler, R., Große, D.; Verifying Next Generation Electronic Systems; Proceedings of the International Conference on Infocom Technologies and Unmanned Systems (ICTUS); Dec 2017

2018

- (5) Onnebrink, G., Leupers, R., Ascheid, G.; ESL Black Box Power Estimation: Automatic Calibration for IEEE UPF 3.0 Power Models; Proceedings of the 2018 Workshop on Rapid Simulation and Performance Evaluation: Methods and Tools; Jan 2018
- (6) Benz, J., Gerum, C., Bringmann, O.; Advancing Source-Level Timing Simulation using Loop Acceleration; Proceedings of the Conference on Design, Automation and Test in Europe; Mar 2018
- (7) Herdt, V., Le., M. H., Große, D., Drechsler, R.; Towards Fully Automated TLM-to-RTL Property Refinement; Proceedings of the Conference on Design, Automation and Test in Europe (DATE); Mar 2018
- (8) Fehmel, T., Stoffel, D., Kunz, W.; Generation of Abstract Driver Models for IP Integration Verification; in *IEEE Transactions on Emerging Topics in Computing*.
- (9) Schwarz, M., Stoffel, D., Kunz, W.; HW/SW Co-Equivalence Checking for Firmware Optimization; Proceedings of the 1st Workshop on Embedded Software for Industrial IoTs (ESIIT 2018), Workshop.
- (10) Strobel, M., Radetzki, M.; Design-Time Optimization Techniques for Low-Power Embedded Memory Subsystems; Proceedings of the 1st Workshop on Embedded Software for Industrial IoT (ESIIT); Mar 2018
- (11) Schwarz, M., Stoffel, D., Kunz, W.; HW/SW Co-Equivalence Checking for Firmware Optimization; Forum on specification & Design Language (FDL 2018), Talk only.
- (12) Herdt, V., Große, D., Le., M. H., Drechsler, R.; Extensible and Configurable RISC-V based Virtual Prototype; Proceedings of the Conference Forum on specification & Design Languages (FDL); Sep 2018
- (13) Strobel, M., Radetzki, M.; Design-Time Optimization Techniques for Low-Power Embedded Memory Subsystems; Proceedings of the 2018 Design and Verification Conference and Exhibition (DVCon); Tutorial; Oct 2018
- (14) Herdt, V., Le., M. H., Große, D., Drechsler, R.; Verifying SystemC using Intermediate Verification Language and Stateful Symbolic Simulation; IEEE Transactions on Computer Aided Design of Integrated Circuits and Systems (TCAD); accepted for publication.
- (15) Martin Dittrich, Daniel Müller-Gritschneider, Ulf Schlichtmann; A Model for the Generation of Firmware and HW/SW Interfaces; Proceedings of Embedded Software for Industrial IoTs (ESIIT 2018), Workshop.
- (16) Daniel Mueller-Gritschneider; Automatic HW-SW-Interface Generation and Optimization; Tutorial Talk on Design and Verification Conference (DVCON) Europe 2018

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- (17) Fehmel, T., Stoffel, D., Kunz, W.; Generation of Abstract Driver Models for IP Integration Verification; in IEEE Transactions on Emerging Topics in Computing, accepted for publication.
- (18) Onnebrink, G., Hallawa, A., Leupers, R., Ascheid, G., and Shaheen, A.; A Heuristic for Multi Objective Software Application Mappings on Heterogeneous MPSoCs, in Proceedings of the Asia South Pacific Design Automation Conference (ASP-DAC) Jan. 2019
- (19) Herdt, V., Le., M. H., Große, D., Drechsler, R.; Maximizing Power State Cross Coverage in Firmware-based Power Management; Proceedings of the Asia South Pacific Design Automation Conference (ASP-DAC); Jan. 2019
- (20) Herdt, V., Große, D., Le., M. H., Drechsler, R.; Verifying Instruction Set Simulators using Coverage-guided Fuzzing; Proceedings of the Conference on Design, Automation and Test in Europe (DATE); Mar 2019
- (21) Herdt, V., Le., M. H., Große, D., Drechsler, R.; Towards Early Validation of Firmware-Based Power Management using Virtual Prototypes: A Constrained Random Approach; Languages, Design Methods, and Tools for Electronic System Design - Selected Contributions from FDL 2017
- (22) Stahl, R., Mueller-Gritschneider, D., Schlichtmann, U.; Firmware-driven Optimization of the Hardware/Software Interface for IoT Nodes; 2nd International Workshop on Embedded Software for Industrial IoT (ESII), Florence, Italy, March 29, 2019

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