

Veröffentlichungen

2008

- (1) Hoelldampf, S.; Zaum, D.; Olbrich, M.; Barke, E.; Neumann, I. & Schmidt, S., Methodologies for high-level modelling and evaluation in the automotive domain, Forum on Specification, Verification and Design Languages, FDL 2008, pp. 73-77, 2008

2009

- (2) Zaum, D.; Hoelldampf, S.; Olbrich, M.; Barke, E.; Neumann, I. & Schmidt, S., The PRAISE approach for accelerated transient analysis applied to wire models, IEEE Behavioral Modeling and Simulation Workshop, 2009. BMAS 2009, pp. 120 -125, 2009

2010

- (3) Hoelldampf, S.; Zaum, D.; Quiring, A.; Olbrich, M. & Barke, E., Beschleunigte Simulation von Mixed-Signal-Schaltungen der Automobilindustrie auf der Grundlage automatisch generierter Modelle, GMM-Fachbericht-ANALOG'10, VDE VERLAG GmbH, 2010
- (4) Zaum, D.; Hoelldampf, S.; Olbrich, M.; Barke, E. & Neumann, I. An accelerated mixed-signal simulation kernel for SystemC, Forum on Specification Design Languages (FDL 2010), 2010, 1-6
- (5) Zaum, D.; Hoelldampf, S.; Olbrich, M.; Barke, E. & Neumann, I. SystemC mixed-signal and mixed-level simulation using an accelerated analog simulation approach, XIth International Workshop on Symbolic and Numerical Methods, Modeling and Applications to Circuit Design (SM2ACD), 2010, 1 -4.

2011

- (6) Hoelldampf, S.; Zaum, D.; Neumann, I.; Olbrich, M. & Barke, E. Fast mixed-signal simulation using SystemC, IEEE International Systems Conference (SysCon), 4-7 April 2011, 527 -530
- (7) Hoelldampf, S.; Zaum, D.; Olbrich, M. & Barke, E. Using analog circuit behavior to generate SystemC events for an acceleration of mixed-signal simulation, IEEE 29th International Conference on Computer Design (ICCD), 9-12 Oct. 2011, 108 -112
- (8) Hoelldampf, S.; Lee, H.-S. L.; Zaum, D.; Olbrich, M. & Barke, E. Efficient Generation of Analog Circuit Models for Accelerated Mixed-Signal Simulation, Proceedings of IEEE International SOC Conference (SOCC), 12-14 Sept. 2012

2015

- (9) C. Radojicic, T. Purusothaman, C. Grimm: Symbolic Simulation of Mixed-Signal Systems with Extended Affine Arithmetic. edaworkshop 2015. Dresden, May 2015.
- (10) C. Radojicic, T. Purusothaman, C. Grimm: Towards Formal Validation: Symbolic Simulation of SystemC Models. DTIS 2015. Naples, Italy, April 2015.
- (11) S. Heinen, e.al: "System Envelope: Bridging the Gap between RF System Design, Circuit Design and SoC Verification" CDN Live 2015 München
- (12) S. Heinen. e.al.: "System-HDL based Virtual Prototyping of RF Transceivers for Verification and IP-Reuse". DAC 2015. San Francisco, CA, Juni 2015
- (13) C. Grimm, C. Radojicic: Verification and Validation of AMS Systems: Towards Coverage of Uncertainties. IMSTW 2015, Paris, France, June 2015.
- (14) C. Radojicic, T. Purusothaman, C. Grimm: Use of Languages for Non-Symbolic Simulation for Symbolic Simulation, FDL 2015, Submitted
- (15) G. Nitsche: Structural Contracts – Towards a Structural Reflection Semantics for Contract Based Design. SIES 2015. Siegen, June 2015 (rejected).

- (16) G. Nitsche and Kim Grüttner, AMS-/EF-Contracts - A Proposal of Contracts for AMS-Verification and AMS-Coverage cation and ams-coverage-analysis, Proceedings of the 2015 Forum on Specification and Design Languages, FDL 2015, Barcelona, Spain, September 14-16, 2015.
- (17) G. Nitsche, K. Gruettner, W. Nebel: Top-Down meets Bottom-Up – Connecting Extra-Functional Semantics with Structural Contracts. MEMOCODE 2015. Austin, USA, September 2015 (submitted).
- (18) G. Gläser, G. Nitsche, and E. Hennig, Temporal decoupling with error-bounded predictive quantum control, Forum on Specification and Design Languages (FDL), Barcelona, Spanien, September 14-16, 2015.
- (19) G. Nitsche, R. Görgen, K. Grüttner, W. Nebel, Structural Contracts - Motivating Contracts to Ensure Extra-Functional Semantics, Proceedings of the fifth IFIP International Embedded Systems Symposium (IESS 2015), Foz do Iguaçu, Brasilien, 11 2015
- (20) Lee, H.-S. L.; Althoff, M.; Hoelldampf, S.; Olbrich, M. & Barke, E., Automated generation of hybrid system models for reachability analysis of nonlinear analog circuits, 20th Asia and South Pacific Design Automation Conference (ASP-DAC), pp. 725-730, 19-22 Jan. 2015
- (21) Lee, H.-S. L.; Olbrich, M. & Barke, E., Analog Mixed-Level Modeling for Accelerated Simulation to Increase the Analog Coverage, Forum on Specification and Design Languages (FDL), September 14-16, 2015
- (22) Xiao Pan, Javier Moreno Molina, and Christoph Grimm, “Modeling power consumption at system-level for design of power integrity-aware AMS-circuits“, In Proceedings of Forum on Specification and Design Languages (FDL), September 14-16, 2015, pages 32-39.

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